# M7 boot in Sirius Project

## Default Status

1. M7 is reset, controlled by register **M7\_RESET\_CTRL[0]**; address = 0x60631020

please refer to $GIT/register definition/Sirius\_top\_global\_registers.xlsx (sheet of RGU register)

1. M7 wait signal is 1’b1, controlled by register **M7\_WAIT[0]**; address = 0x40b00008

please refer to $GIT/register definition/m7\_top\_global\_registers.xlsx

1. M7 boot vector table is programmable, controlled by register **M7\_INITVTOR[24:0]**; address = 0x40b00020

please refer to $GIT/register definition/m7\_top\_global\_registers.xlsx

## M7 Boot

In A7 code, you should initial M7 to release it.

*Write\_reg32(0x40b00020, 0x0); // config INITVTOR, boot from ITCM*

*tmp = read\_reg32(0x60631020);*

*Write\_reg32(0x60631020, tmp | 0x1); //release M7 reset*

*Write\_reg32(0x40b00008, 0x0); //release wait*